



Date: April 13, 2004

Page 1 of 1

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE (REV. 05/03)		ATTY. DOCKET NO. TI-36484 (UNITI-176XX)	APPLICATION NO. 10/748,848
INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>			
APPLICANT: Chaun Ni et al			
FILING DATE December 30, 2003		TC ART UNIT Unknown	2838

#### U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION/ ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE
↑ JLJ	US 6,204,706	3/20/2001	Horvath	327	198	_____
JLJ	US 5,610,503	3/11/1997	Fogg et al.	323	283	_____
JLJ	US 6,430,070	8/06/2002	Shi et al.	363	97	_____
JLJ	US 6,583,520	6/24/2003	Shi et al	307	33	_____
↓	US 6,418,002	7/9/2002	Yang et al.	361	90	_____
	US					
	US					

#### FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES      NO

#### OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)

JLJ	Dual, Voltage Mode, DDR Selectable, Synchronous, Step-Down Controller For Notebook System Power, Texas Instruments, 2003 July

EXAMINER <i>Sterrett</i>	DATE CONSIDERED <i>9/28/05</i>
*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	